

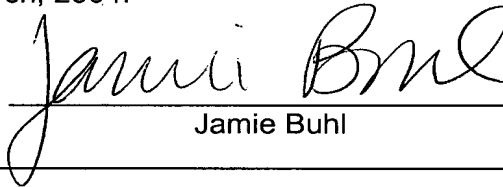


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Angelo Magri', Ferruccio Frisina
Title of Invention: Insulated Gate Planar Integrated Power Device With Co-Integrated Schottky Diode and Process
Serial No.: 10/713,778
Filing Date: November 14, 2003
Attorney Dkt. No.: 02-CT-372/DP (2110-86-3)

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail and is addressed to Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 5th day of March, 2004.



Jamie Buhl

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

March 5, 2004

Dear Commissioner:

In compliance with the duty of disclosure under 37 CFR § 1.56, Applicants submit herewith patents, publications, or other information for consideration during the examination of this application.

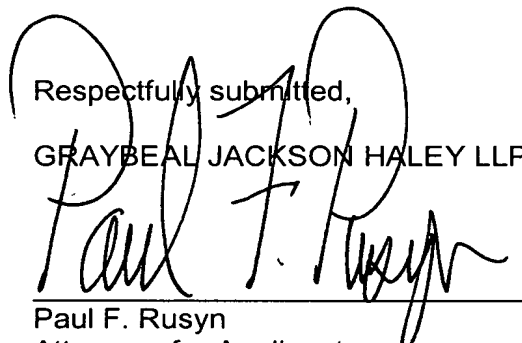
In accordance with 37 CFR § 1.97, the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that a search has been made nor that the information cited in the statement is or is considered to be "material" to


patentability as defined in 37 CFR § 1.56(b).

Certification Under Section 1.97(e)

The undersigned hereby certifies that this information was not known to individuals associated with the filing or prosecution of the subject patent application in accordance with Section 1.56(c) more than three months prior to the filing of this statement.

No additional costs are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 07-1897.

Respectfully submitted,
GRAYBEAL JACKSON HALEY LLP

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Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)		Docket Number (Optional) 02-CT372/DP; 2110-86-3		Application Number 10/713,778	
		Applicant(s) Angelo Magri, et al.			
		Filing Date November 14, 2003		Group Art Unit 2811	

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4,823,172	4/18/1989	Teruyoshi Mihara	357	15	6/10/1987
	5,528,058	6/18/1996	Douglas A. Pike, et al.	257	142	10/13/1994

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.